



# Computer Organization and Design MIPS Edition, Fifth Edition: The Hardware/Software Interface (The Morgan Kaufmann Series in Computer Architecture and Design)

By David A. Patterson, John L. Hennessy

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**Computer Organization and Design MIPS Edition, Fifth Edition: The Hardware/Software Interface (The Morgan Kaufmann Series in Computer Architecture and Design)** By David A. Patterson, John L. Hennessy

*Computer Organization and Design, Fifth Edition*, is the latest update to the classic introduction to computer organization. The text now contains new examples and material highlighting the emergence of mobile computing and the cloud. It explores this generational change with updated content featuring tablet computers, cloud infrastructure, and the ARM (mobile computing devices) and x86 (cloud computing) architectures. The book uses a MIPS processor core to present the fundamentals of hardware technologies, assembly language, computer arithmetic, pipelining, memory hierarchies and I/O. Because an understanding of modern hardware is essential to achieving good performance and energy efficiency, this edition adds a new concrete example, *Going Faster*, used throughout the text to demonstrate extremely effective optimization techniques. There is also a new discussion of the Eight Great Ideas of computer architecture. Parallelism is examined in depth with examples and content highlighting parallel hardware and software topics. The book features the Intel Core i7, ARM Cortex-A8 and NVIDIA Fermi GPU as real-world examples, along with a full set of updated and improved exercises.

This new edition is an ideal resource for professional digital system designers, programmers, application developers, and system software developers. It will also be of interest to undergraduate students in Computer Science, Computer Engineering and Electrical Engineering courses in Computer Organization, Computer Design, ranging from Sophomore required courses to Senior Electives.

- Winner of a 2014 Texty Award from the Text and Academic Authors Association
- Includes new examples, exercises, and material highlighting the emergence of mobile computing and the cloud
- Covers parallelism in depth with examples and content highlighting parallel hardware and software topics

- Features the Intel Core i7, ARM Cortex-A8 and NVIDIA Fermi GPU as real-world examples throughout the book
- Adds a new concrete example, "Going Faster," to demonstrate how understanding hardware can inspire software optimizations that improve performance by 200 times
- Discusses and highlights the "Eight Great Ideas" of computer architecture: Performance via Parallelism; Performance via Pipelining; Performance via Prediction; Design for Moore's Law; Hierarchy of Memories; Abstraction to Simplify Design; Make the Common Case Fast; and Dependability via Redundancy
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**(The Morgan Kaufmann Series in Computer Architecture and Design) By David A. Patterson, John L. Hennessy Bibliography**

- Sales Rank: #45615 in Books
- Brand: Brand: Morgan Kaufmann
- Published on: 2013-10-10
- Original language: German, English, French, Italian, Spanish
- Number of items: 1
- Dimensions: 9.10" h x 1.30" w x 7.40" l, 2.86 pounds
- Binding: Paperback
- 800 pages

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